

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1129	331/57.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/30 08:58
L2	495	331/185.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/30 10:33
L3	274	331/186.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/30 11:38
L4	329	331/45.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/30 12:13
L5	75	327/272.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/30 12:28
L6	274	327/278.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/30 12:37
L7	135	327/254.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/30 12:44
L8	180	327/281.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/30 12:44
L9	19604	(delay stage switch gate transistor current source)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2006/06/30 12:57
L10	25	(delay stage switch gate transistor current source).clm.	US-PGPUB	AND	ON	2006/06/30 12:56

## EAST Search History

L12	121	(delay adj1 stage) switch (gate near transistor) (current adj1 source)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2006/06/30 13:01
S1	7760	delay near stage\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/30 08:23
S2	3	switch\$1controlled adj2 load	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/13 10:49
S3	0	(delay near stage\$1) and (switch\$1controlled adj2 load)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/13 10:50
S4	7499	switch near load	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/13 10:50
S5	1735	noise near power near supply	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/13 10:51
S6	0	((delay near stage\$1) and (switch near load)) and (noise near power near supply)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/13 10:51
S7	22	(delay near stage\$1) and (switch near load)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/13 11:08
S8	3	"6373342"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/13 11:08
S9	9239	transistor near capacitor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/14 08:49

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S10	6678	transistor adj1 capacitor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/14 08:50
S11	9	(transistor adj1 capacitor) near delay	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/14 08:50
S12	1202	(transistor adj1 capacitor) and delay	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/14 08:52
S13	0	capacitive near load near output near delay near cell	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/31 14:50
S14	4	capacitive near load near delay near cell	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/31 14:53
S15	2157	(capacitive and load and delay and (switch or switch\$1control\$3) and (power or supply))	US-PGPUB	OR	ON	2006/01/31 14:55
S16	25	(capacitive and load and delay and (switch or switch\$1control\$3) and (power or supply)).clm.	US-PGPUB	OR	ON	2006/01/31 14:55